

Patent

Attorney Docket No.: MTI-31041

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Er-Xuan Ping

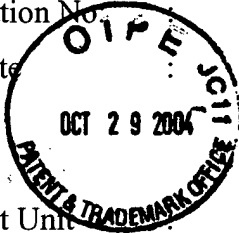
Serial No. : 09/816,962

Confirmation No. 8677

Filing Date March 23, 2001

For: Method for Forming Raised Structures by Controlled Selective Epitaxial Growth of Facet Using Spacer

Group Art Unit 2812



CERTIFICATION UNDER 37 CFR 1.8(a) and 1.10

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 as "Express Mail Post Office to Addressee" Mailing Label No. EV EV405079255US.

Commissioner for Patents
P.O. Box 1450
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10.29.04

John R. Hawk

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97/98

Sir:

In compliance with Applicant's duty of disclosure as set forth in 37 C.F.R. §1.56, listed on the attached Form PTO-1449 are those patents and other publications known to Applicant which may be considered material to the patentability of the claims of the above-identified application.

The Applicant respectfully requests that the documents listed on the attached equivalent to Form PTO-1449 be considered by the Examiner, that these references be made of record in the present applicant, and that an initialed copy of the attached equivalent to Form PTO-1449 be returned to the undersigned in accordance with MPEP 609.

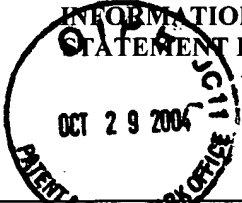
Respectfully submitted,

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Dated: October 29, 2004

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Alexandria, VA 22313 	ATTY. DOCKET NO.	Serial No.
	MTI-31041	09/816,962
	Applicant	Confirmation No.
	Er-Xuan Ping	8677
	Filing Date	Group Art Unit
	March 23, 2001	2812

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Publication/ Issue Date	Patentee	U.S. Class	Sub- Class
	A1	6,594,293 B1	07-15-03	Bulsara	372	43
	A2	6,509,239 B1	01-21-03	Nuttall et al.	438	300
	A3	6,495,437 B1	12-17-02	Yu	438	591
	A4	6,479,875 B1	11-12-02	Gonzalez	257	396
	A5	6,458,699 B1	10-01-02	Nuttall et al.	438	675
	A6	6,159,852	12-12-00	Nuttall et al.	438	674
	A7	6,072,226	06-06-00	Thakur et al.	257	506
	A8	5,888,294	03-30-99	Min et al.	117	84
	A9	5,841,150	11-24-98	Gonzalez et al.	257	3
	A10	5,780,906	07-14-98	Wu et al.	257	368
	A11	5,360,760	11-01-94	Hayashi	117	93
	A12	5,057,888	10-15-91	Fazan et al.	357	23.6
	A13	2001/0040292 A1	11-15-01	Hahn et al.	257	750

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initial		Non-Patent Document
	B1	Blanton, T.N., et al., The Rigaku Journal, Vol. 13, No. 1, 1996, pp. 3-7
	B3	Van Zant, Peter, Microchip Fabrication, A Practical Guide to Semiconductor Processing, 4 th Ed., McGraw-Hill, New York, NY (2000), Chapter 12 at pages 380-388.
	B4	Wolf et al., Silicon Processing for the VLSI Era, Vol. 1: Process Technology, 2 nd Ed., Lattice Press, Sunset Beach, CA (2000), Chapter 7 (Silicon Epitaxial Growth and Silicon on Insulator) at pages 225-264.

Examiner Initials	Date Considered
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	